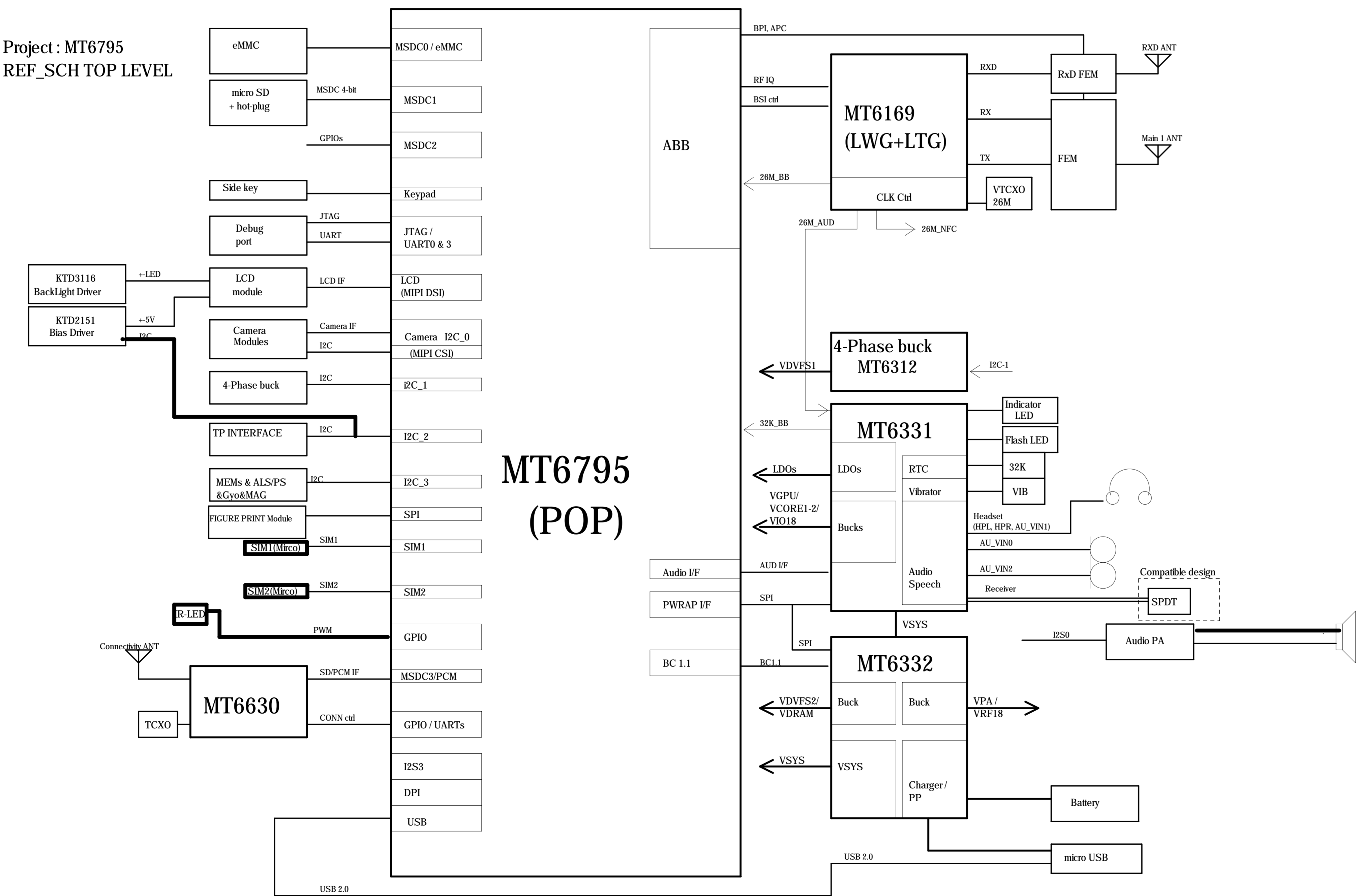
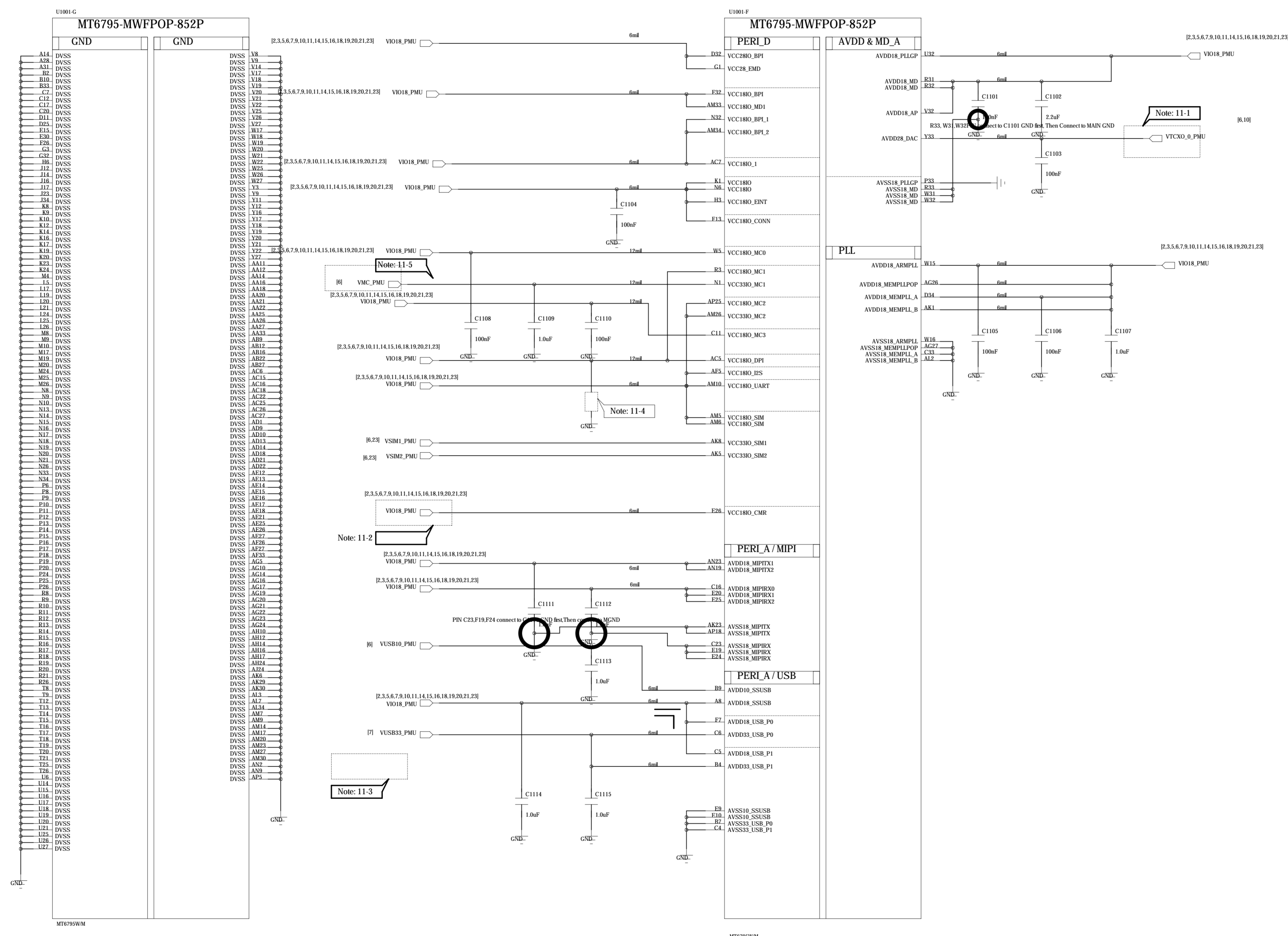


Project : MT6795
REF_SCH TOP LEVEL



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Schematic design notice of "11_BB_POWER" page.

Note 11-1: AVDD28_DAC (Y33 ball) must be powered by "VTCXO_1_PMU".

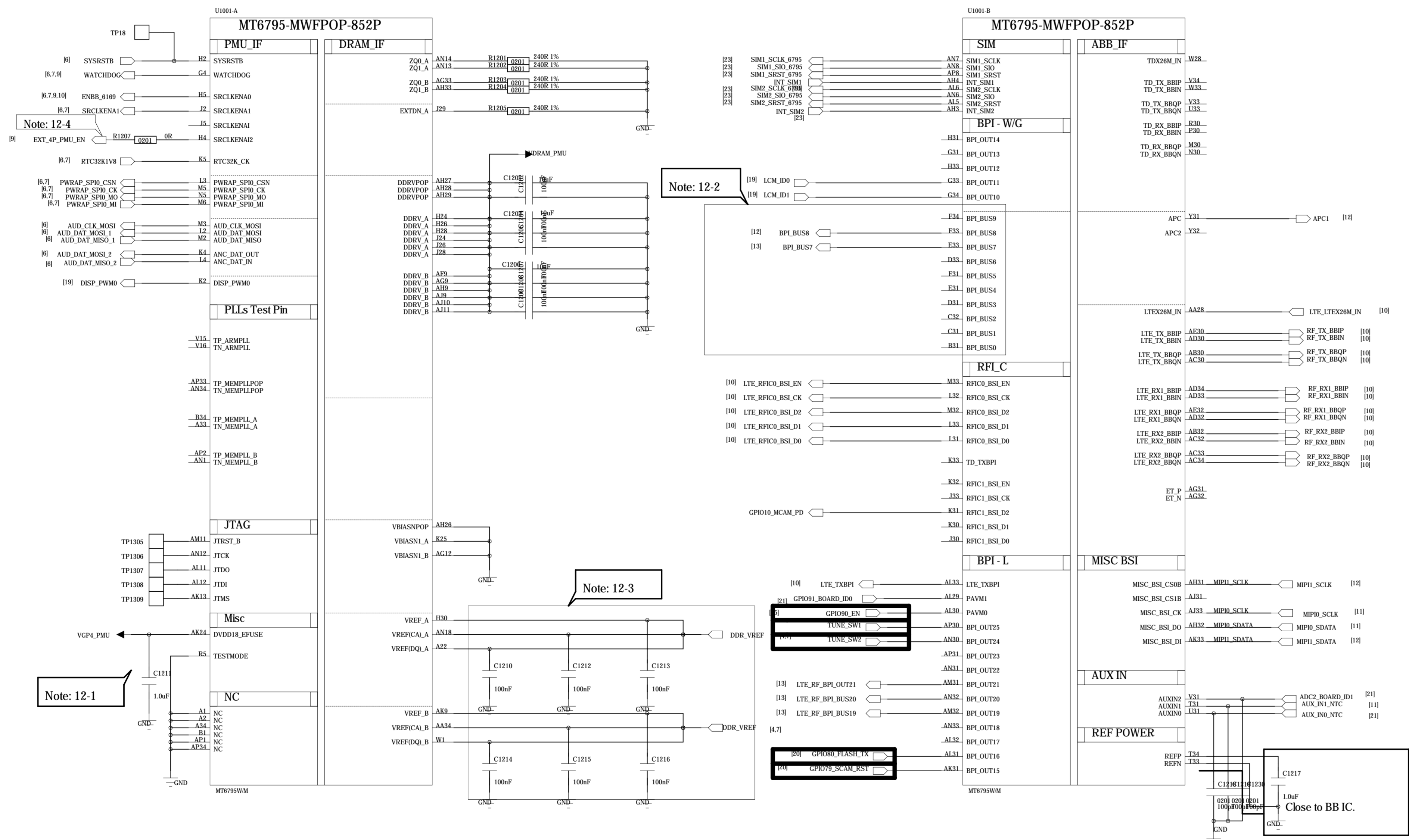
Note 11-2: Connect VCC18IO_CM (E26 ball) to "VIO18_PMU" since I2C-2 I/O power is powered by VCC18IO_CM.

Note 11-3: Connect AVDD33_USB_P1 (B4 ball) to "VSIM1_PMU" for IC-USB / Samrt card application.
Connect AVDD33_USB_P1 (B4 ball) "VUSB33_PMU" for USB application.

Note 11-4: Reserve 0.1uF decoupling cap in VCC18IO_DPI for MHL.

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		TITLE: <div><Title></div>				
DRAWN: <div><Drawn By></div>	DATED: <div><Drawn Date></div>					
CHECKED: <div><Checked By></div>	DATED: <div><Checked Date></div>	CODE: <div><Code></div>	SIZE: <div>A0</div>	DRAWING NO.: <div><Drawing Number></div>	REV: <div><Revision></div>	
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RELEASED: <div><Released By></div>	DATED: <div><Release Date></div>	SCALE: <div><Scale></div>	SHEET: 86		23	

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Schematic design notice of "11_BB_11" page.

Note 12-1: Apply 1.8V to DVDD18_EFUSE (AK24) for eFuse programming.

Note 12-2: The BPL_BUS0-BPL_BUS9 are capable of 2.8V I/O operation.

Note 12-3: The de-coupling cap. of DRAM VREF have to be placed as close to BB as possible.

Note 12-4: SRCLKENAI2 features watch dog reset output to reset 4-phase buck.
R1207 = 0R when BOM option of U2401 is DA9210.
R1207 = NC when BOM option of U2401 is 2nd source.

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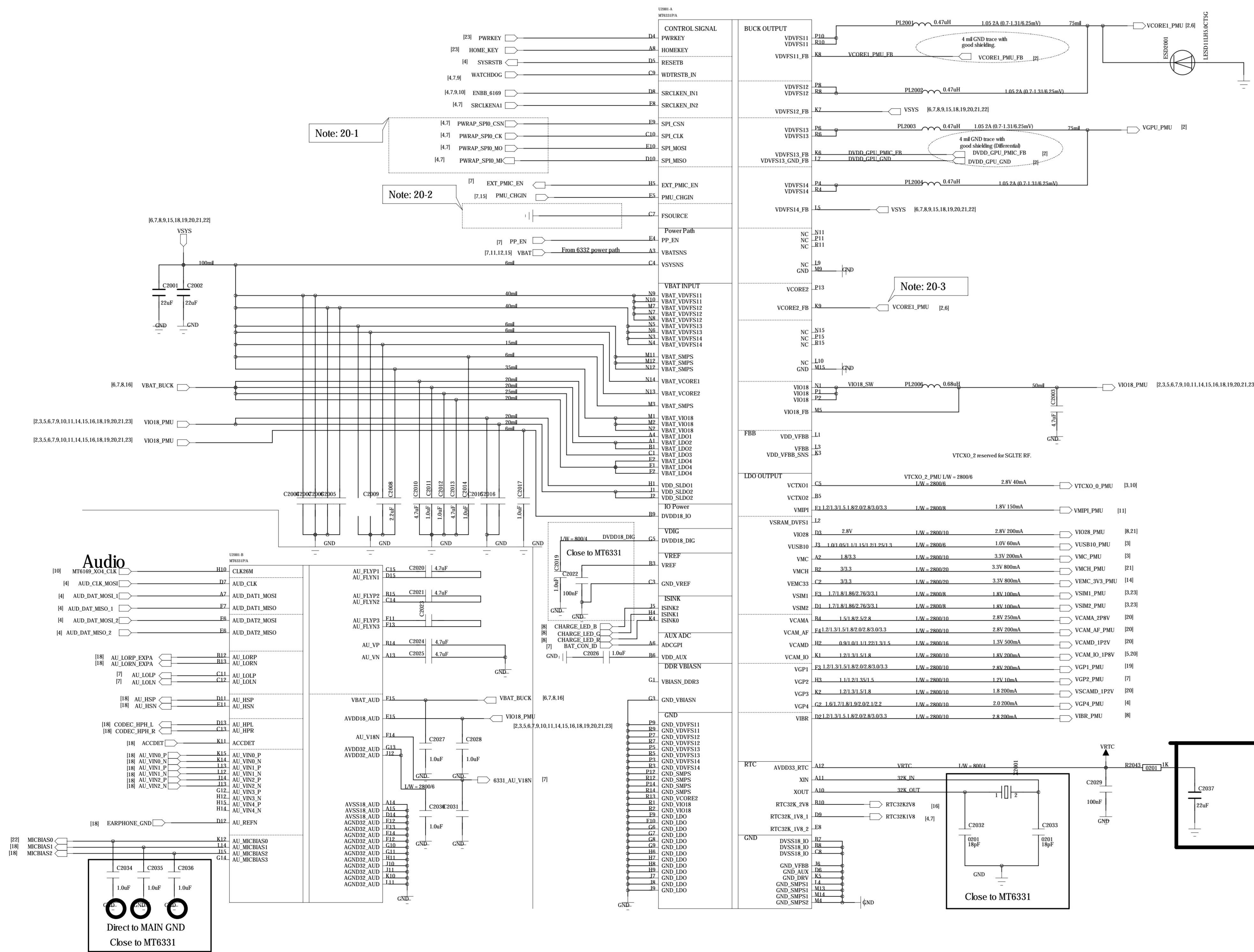
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Schematic design notice of "20_POWER_MT6331" page.

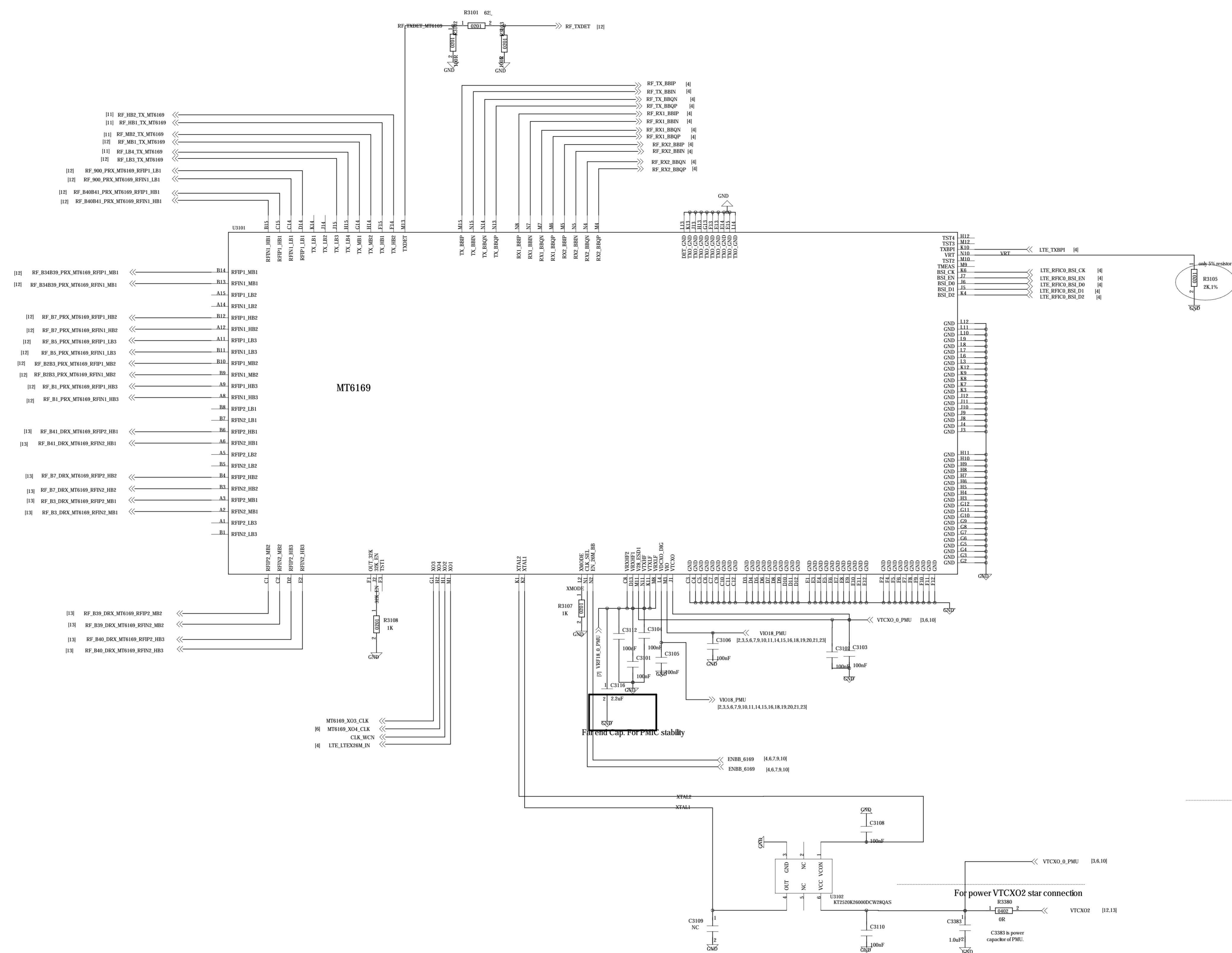
Note 20-1: External pull resistor in PMU SPI interface is not allowed.

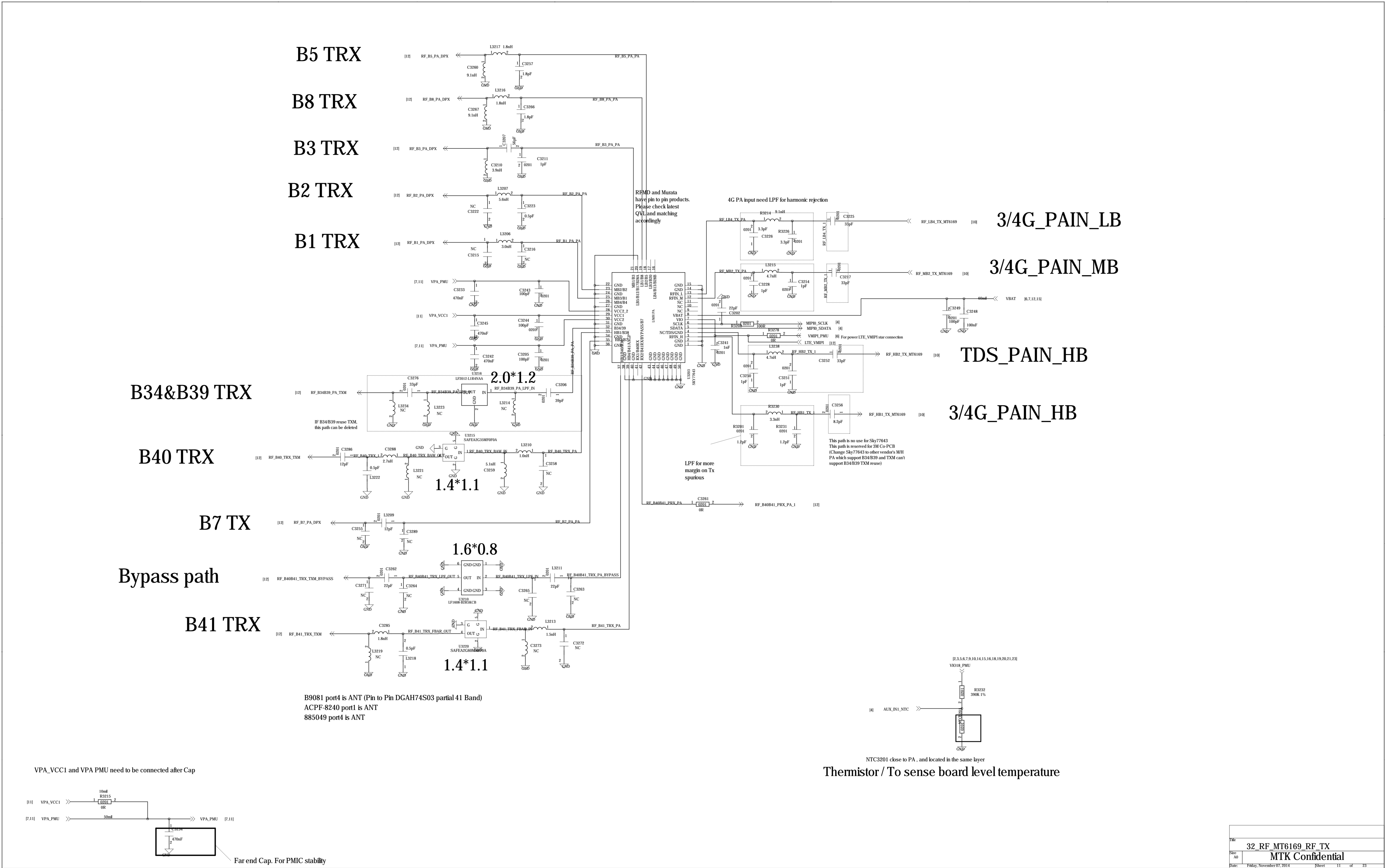
Note 20-2: PMU_FSOURCE high(DVDD18_DIG)-> EFUSE program.

Note 20-3: Connect MT6331's K9 pin to "VCORE1_PMU" when VCORE2 is not used.



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RELEASED	<Released By>	DATE	<Release Date>
SCALE		SHEET 6/ 23	





ASM_Main

2.0*2.0*0.6mm

SP16T

2.5*2.5*1.5mm

2G_PAIN_HB

2G_PAIN_LB

??????????????

B3 TRX

B2 TRX

B7 TRX

1.8*1.4

B1 TRX

1.8*1.4

B5 TRX

1.8*1.4

B40/B41 PRX

B8 TRX

1.8*1.4

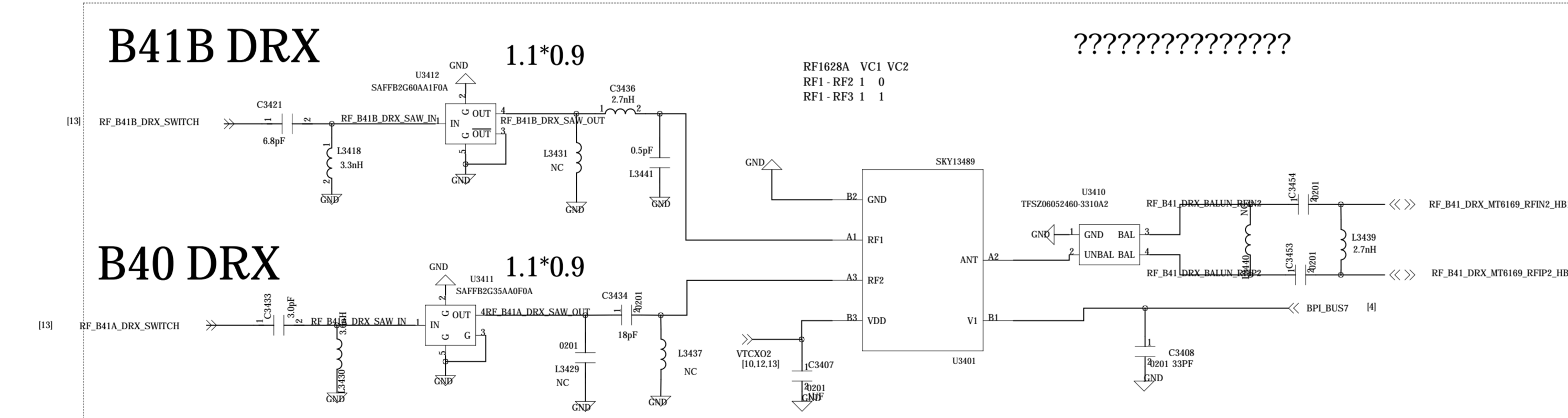
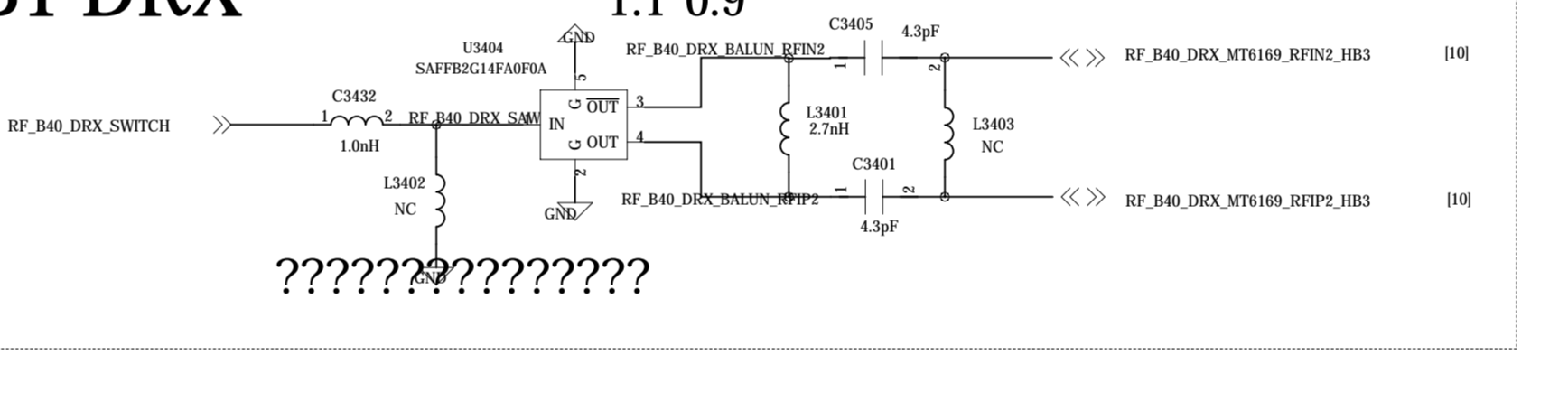
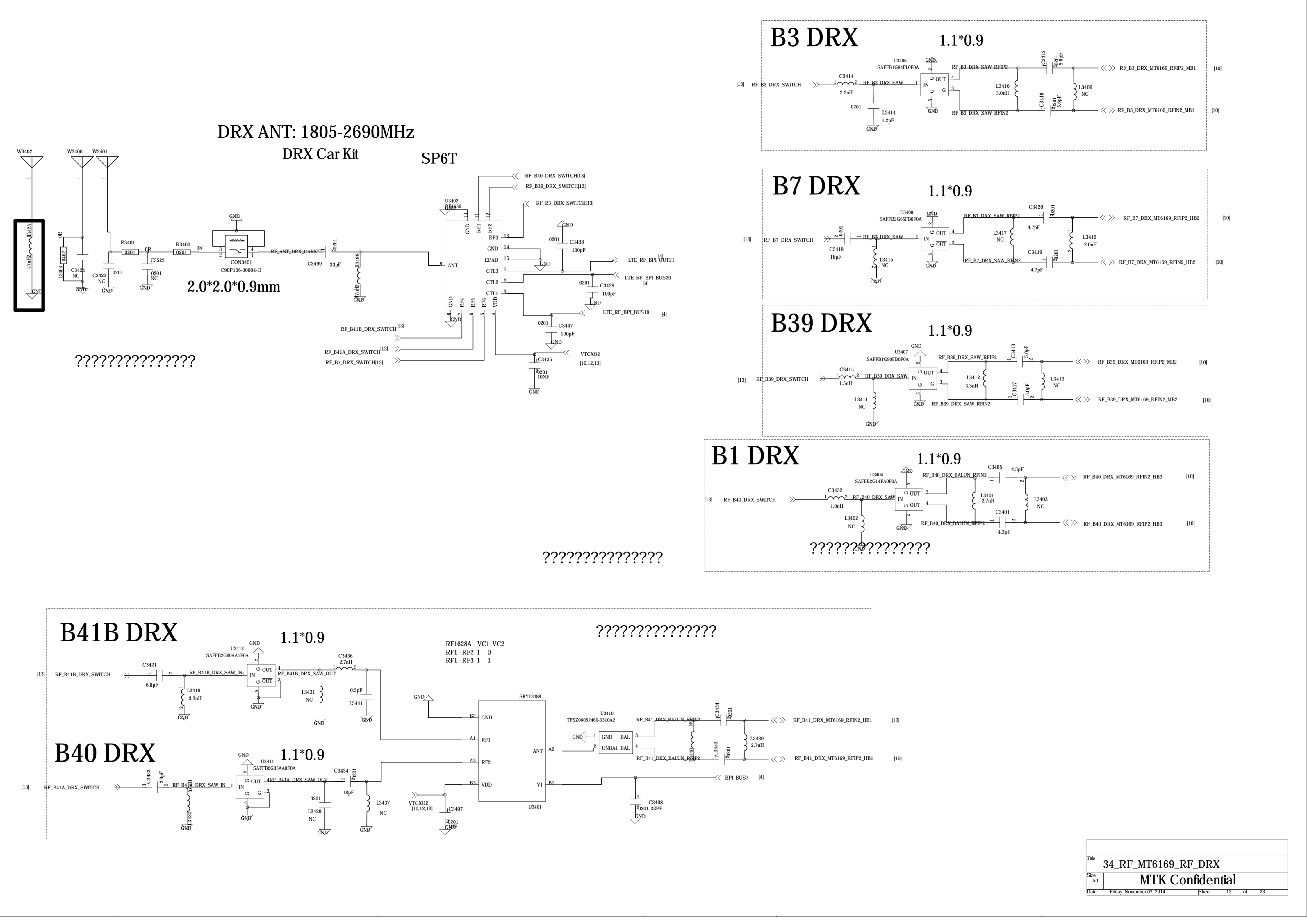
B34&B39 PRX

1.5*1.1

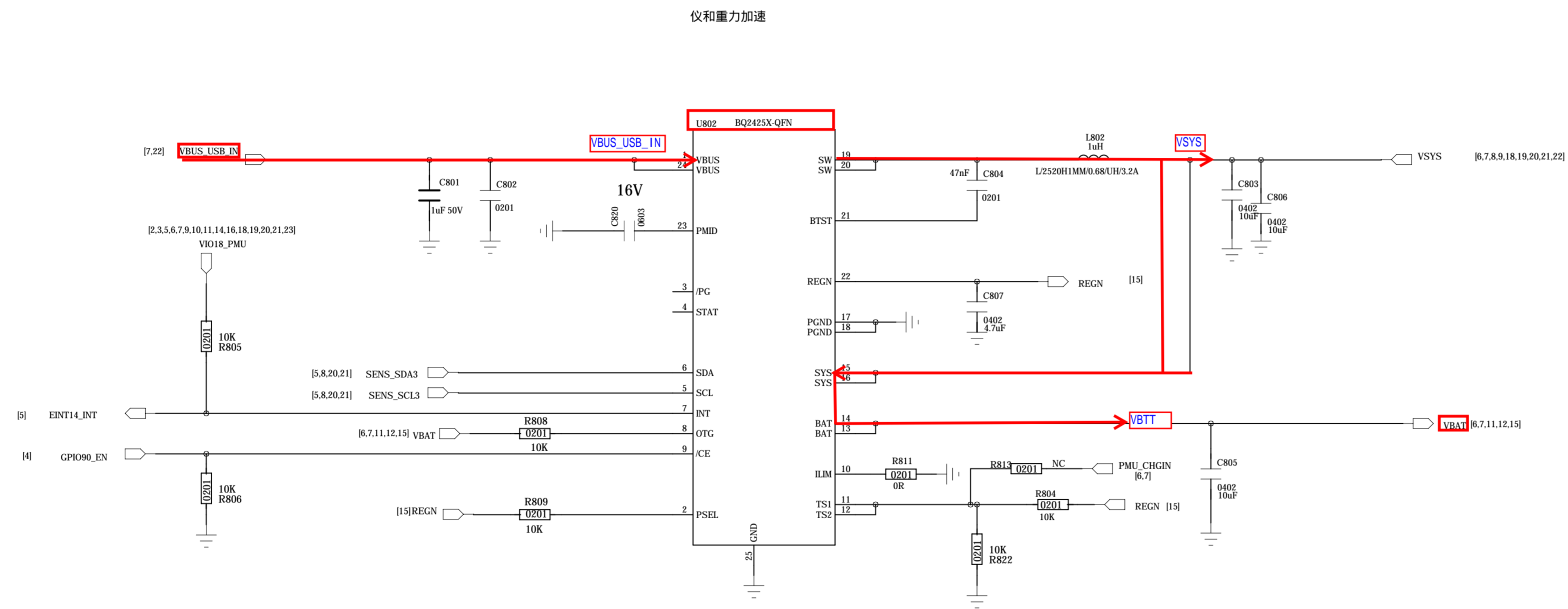
33_RF_MT6169_RF_PRX

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Schematic design notice of "41_MEMORY_SD Card" page.

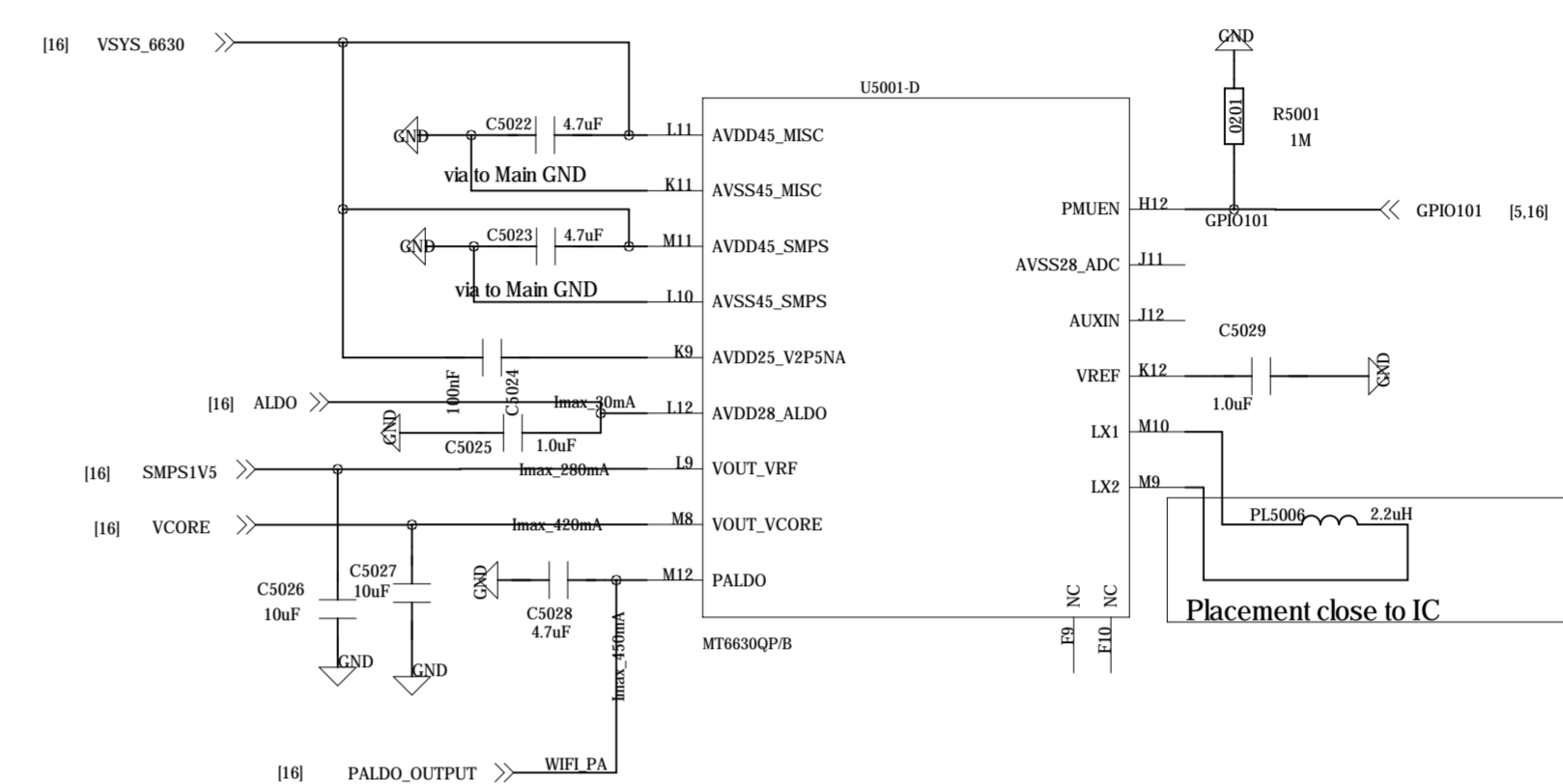
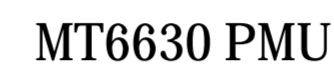
Note 41-1: The equivalent capacitance of ESD protection device must be <=1pF
-- otherwise it will result in NFC card mode function fail.

Note 41-2: Depends on system design to add ESD protection component or not.

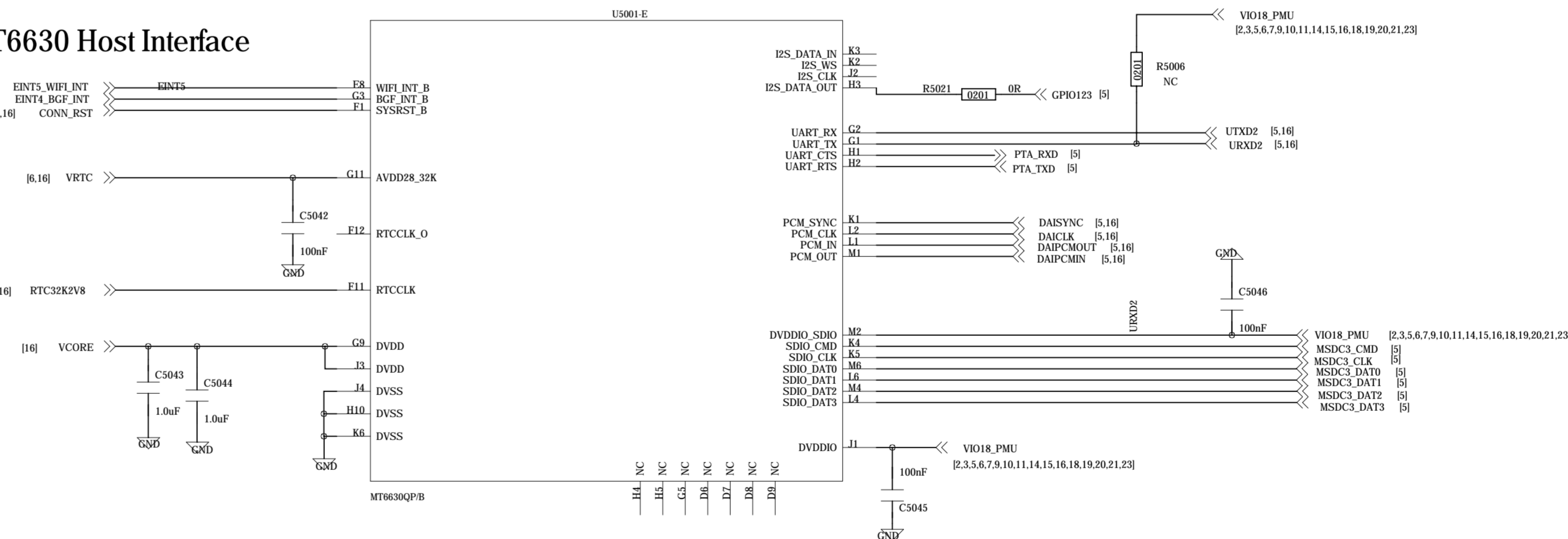
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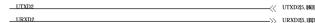
1.4*1.2

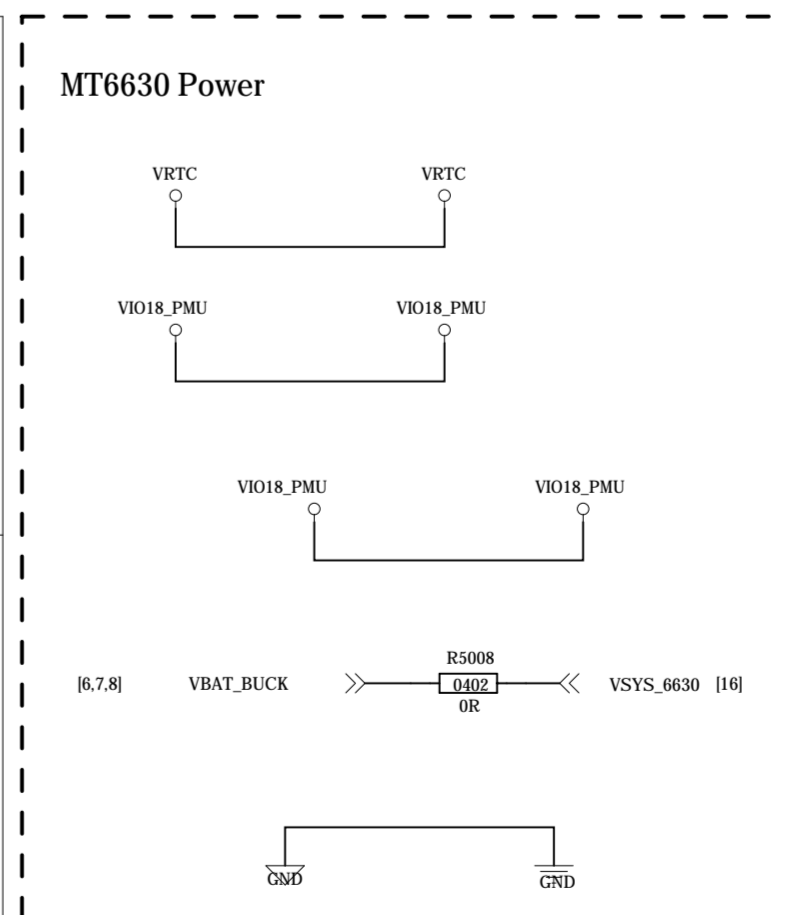
Schematic diagram of the 1.4*1.2 module. It features two 15001 comparators and a 15002 OR gate. The left comparator (L5001) has its NC pin connected to GND, C5014 connected to GND, and Q5014 connected to GND. The right comparator (L5002) has its NC pin connected to NC, C5012 connected to GND, and Q5012 connected to GND. The outputs of both comparators (Q5014 and Q5012) are connected to the inputs of the OR gate (L5002). The output of the OR gate (L5002) is connected to the output pin OUT of the 15002 package.



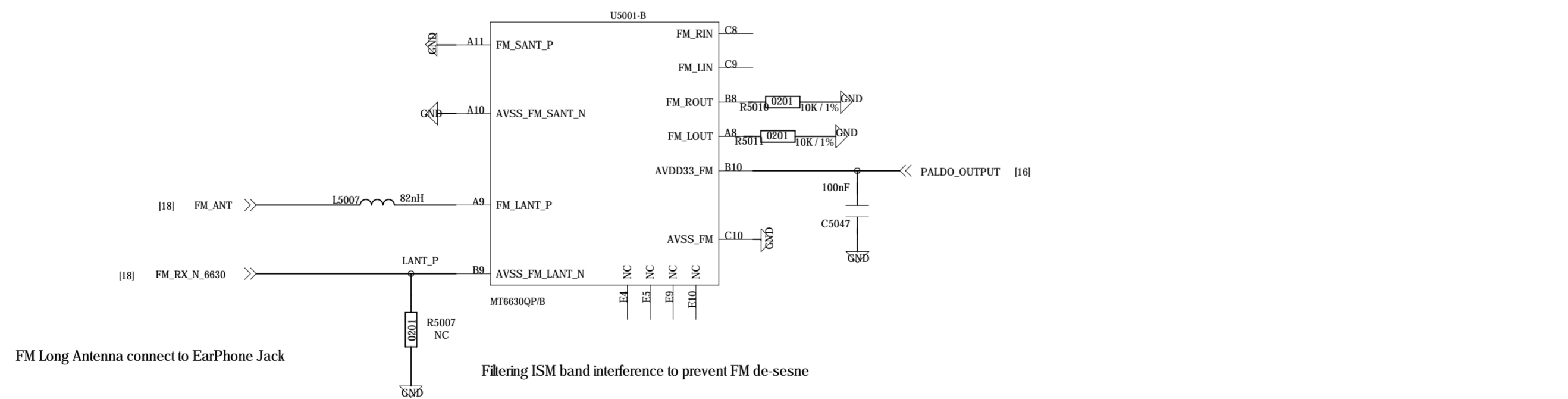
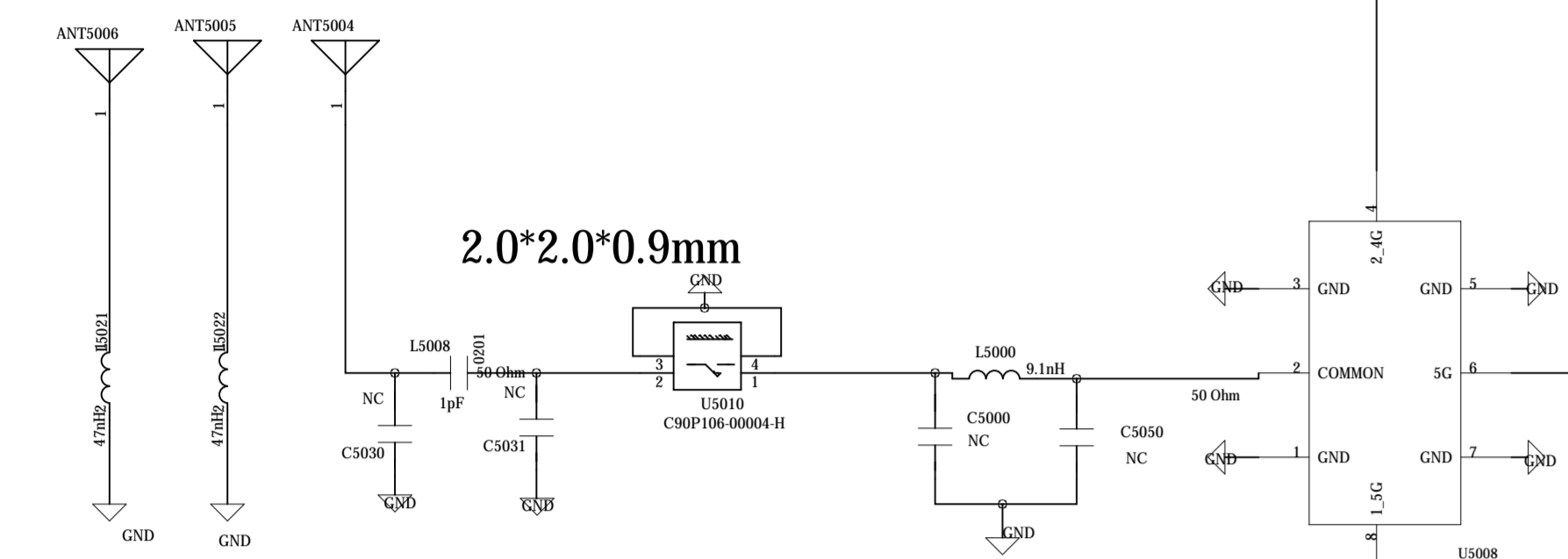
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<p>MT6630 UART</p> 	<p>MT6630 GPIO & RTC & GPS SYNC</p> 
<p>MT6630 PCM</p> 	

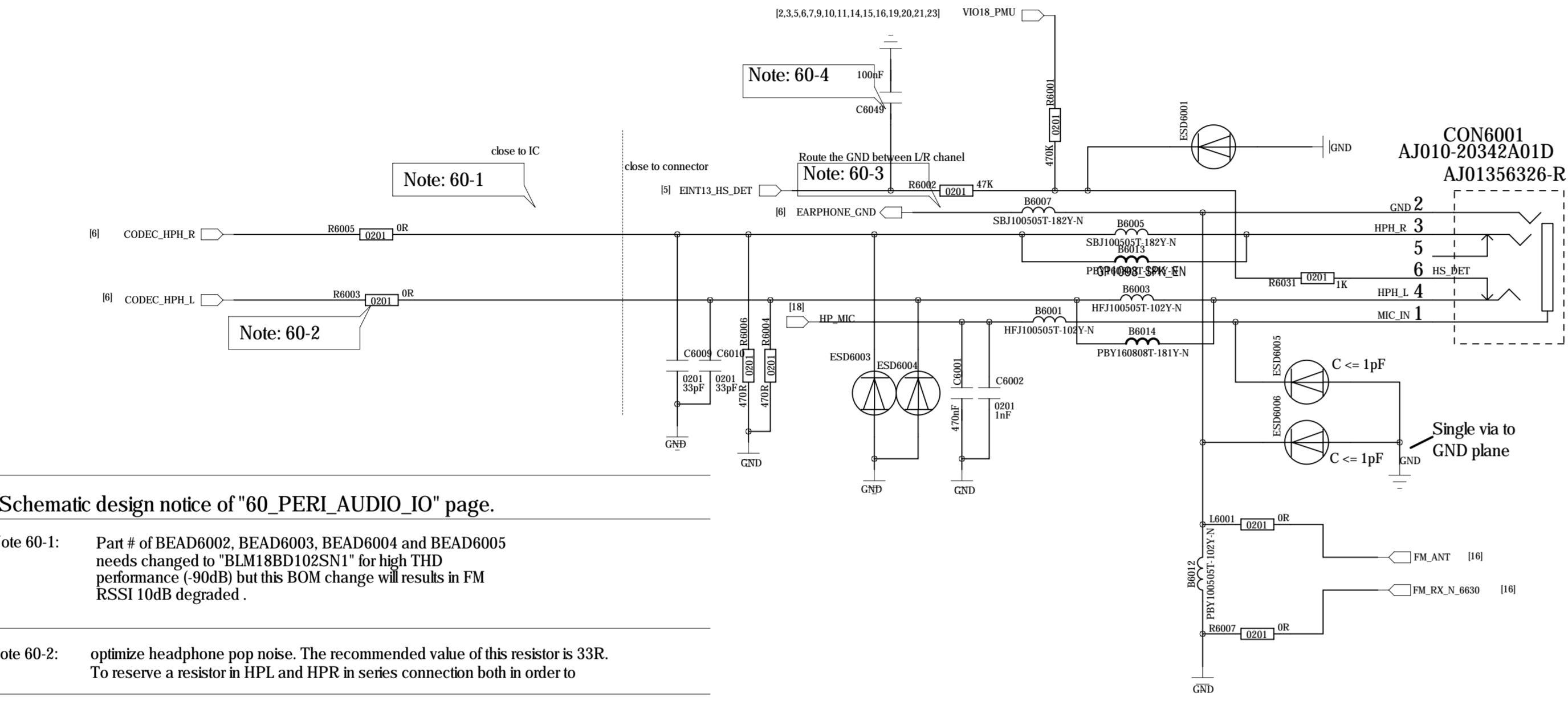


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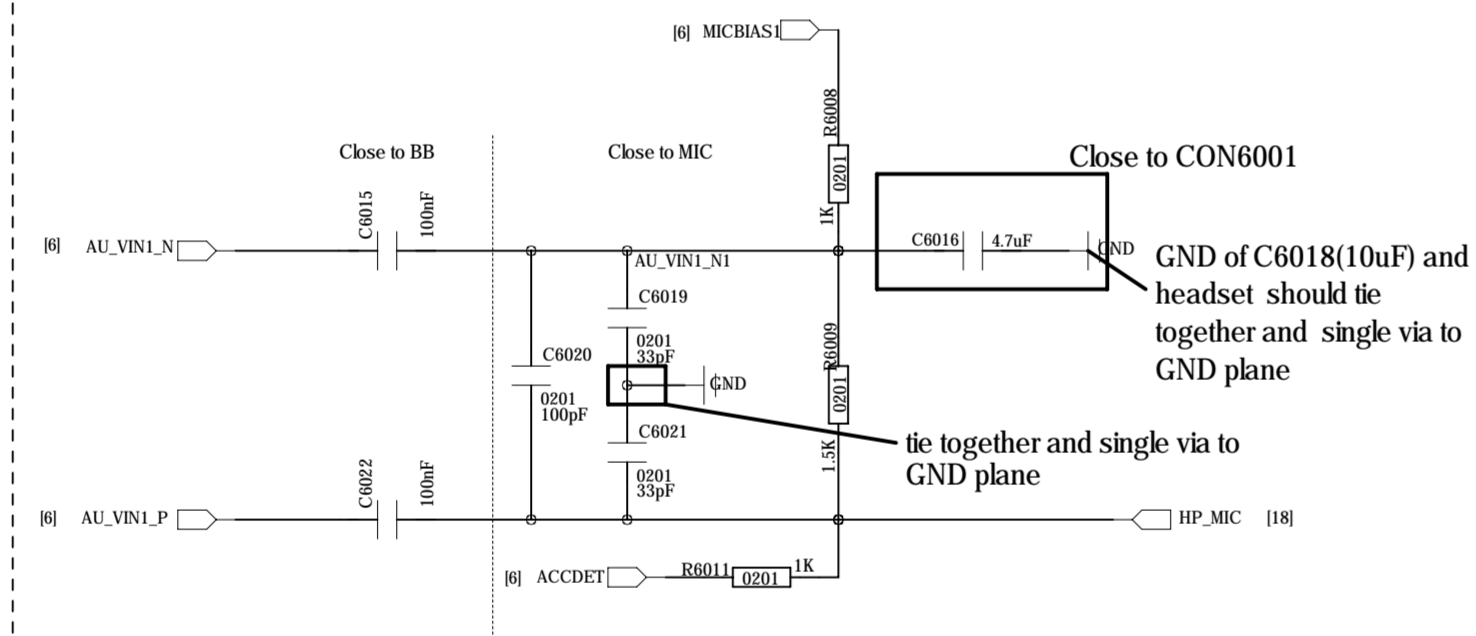


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Earphone Audio

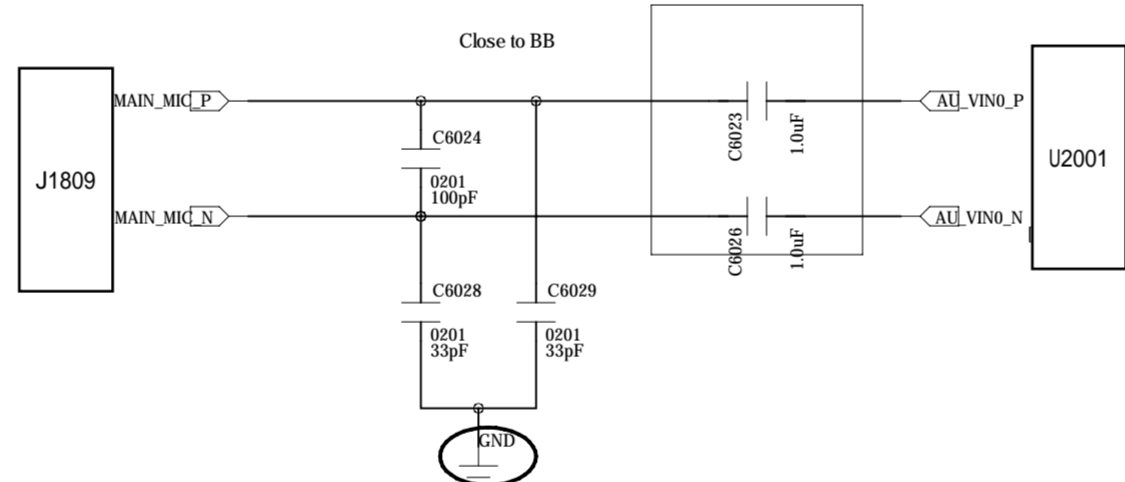


Earphone MICPHONE



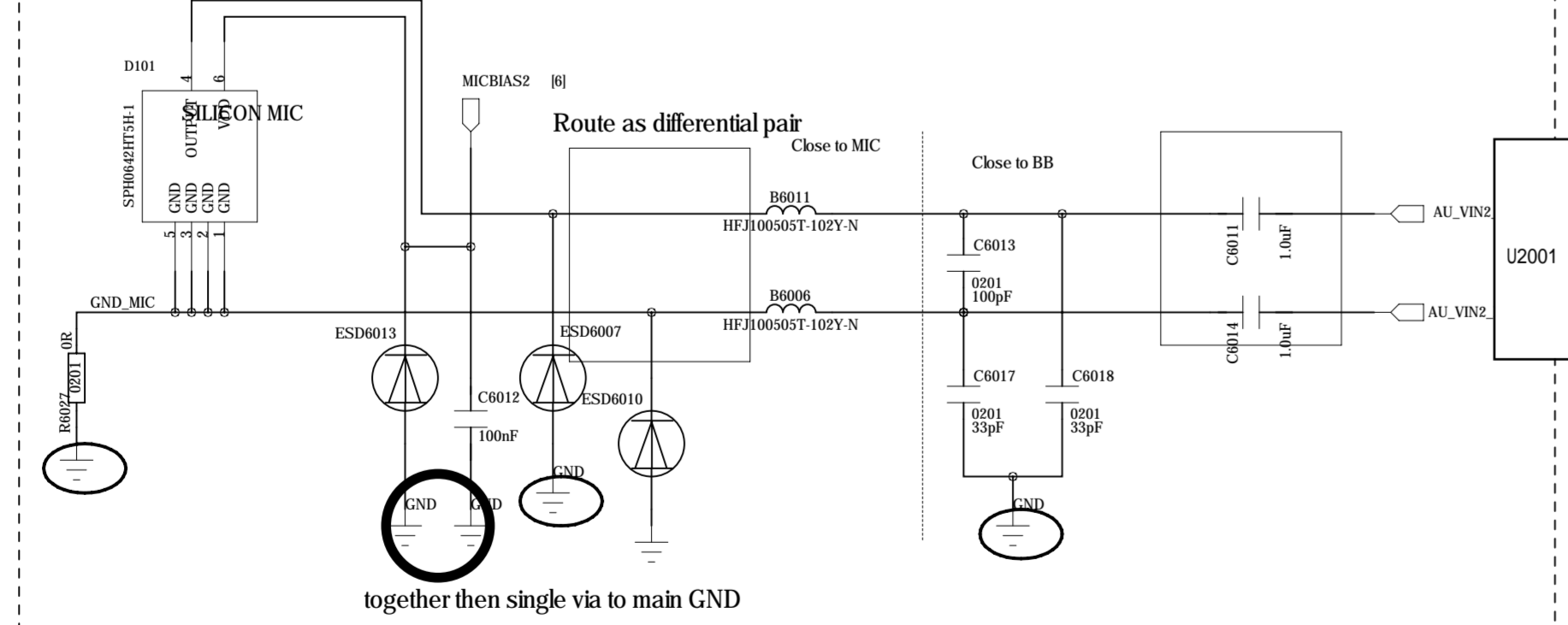
Main microphone

Analog MIC

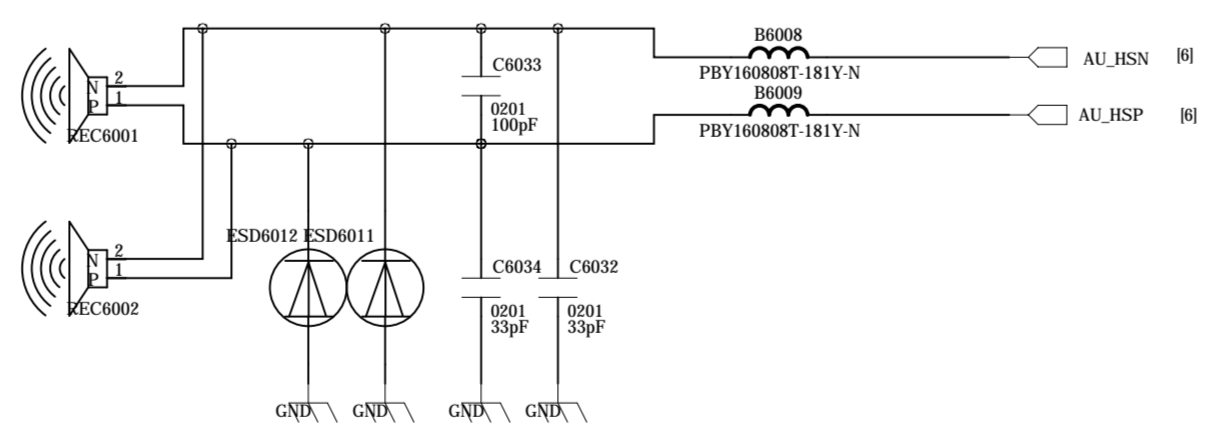


Secondary MIC

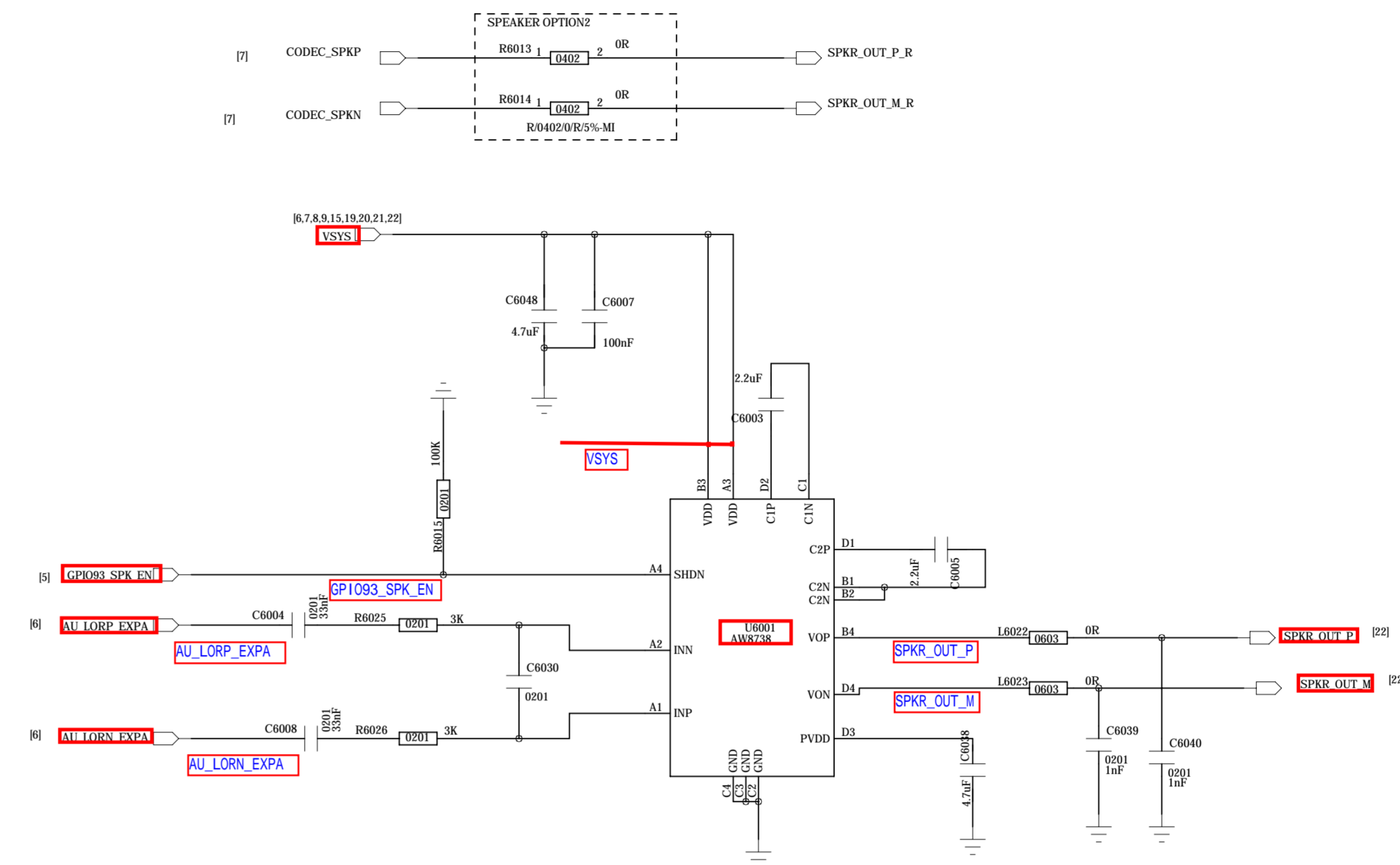
CZE 8401SR-423G-XDRAC-BD-OMIC



REC



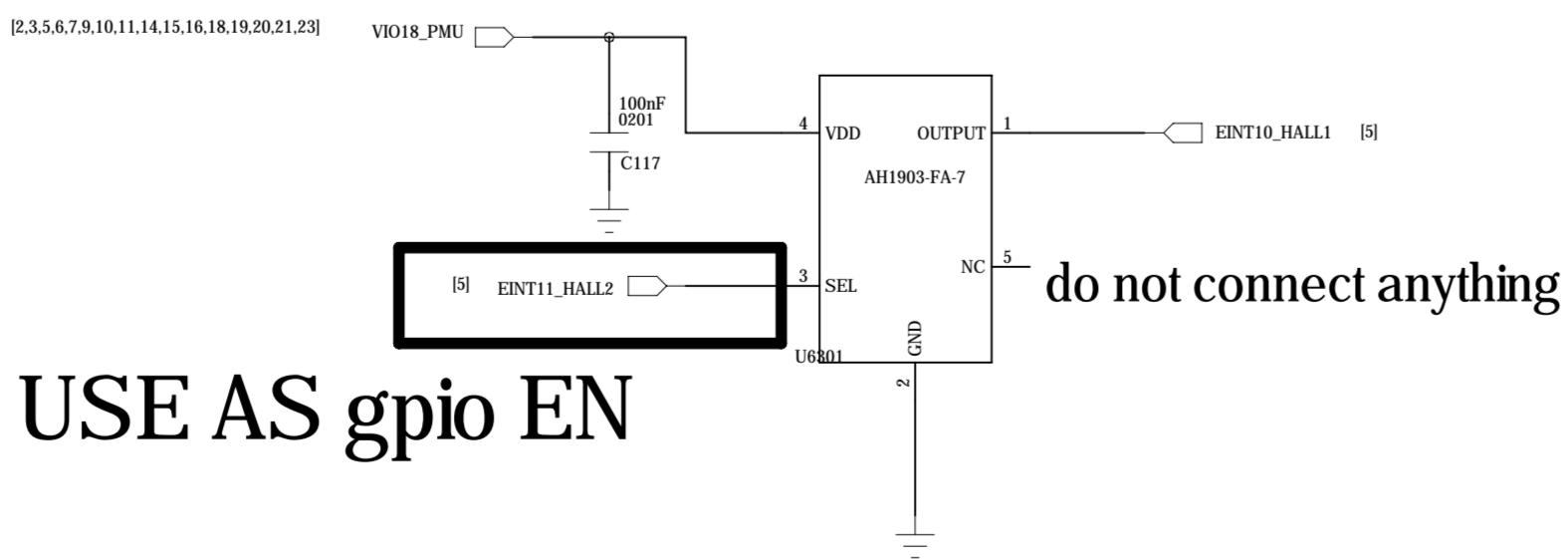
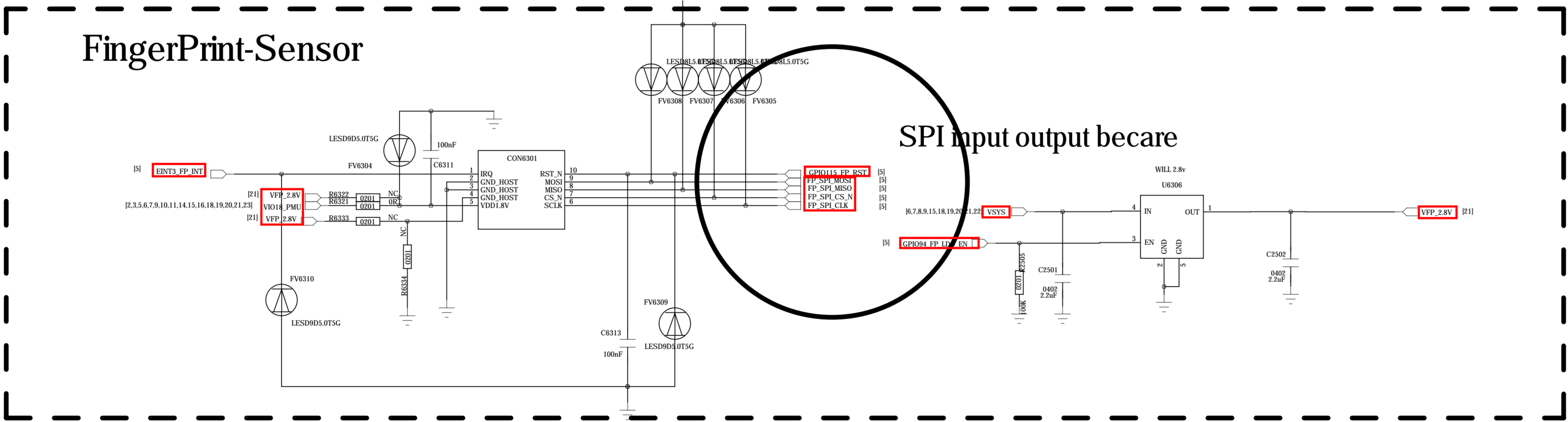
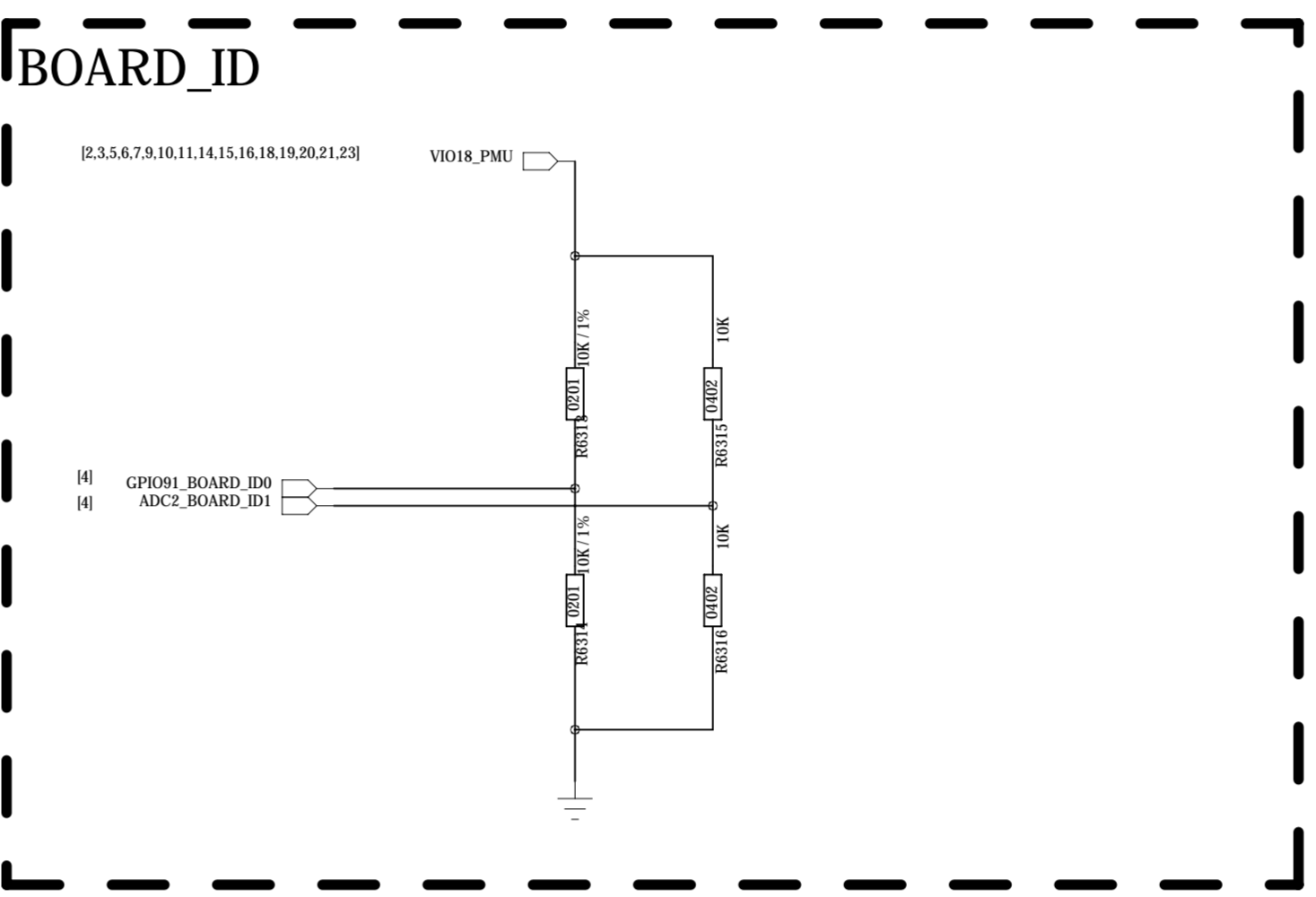
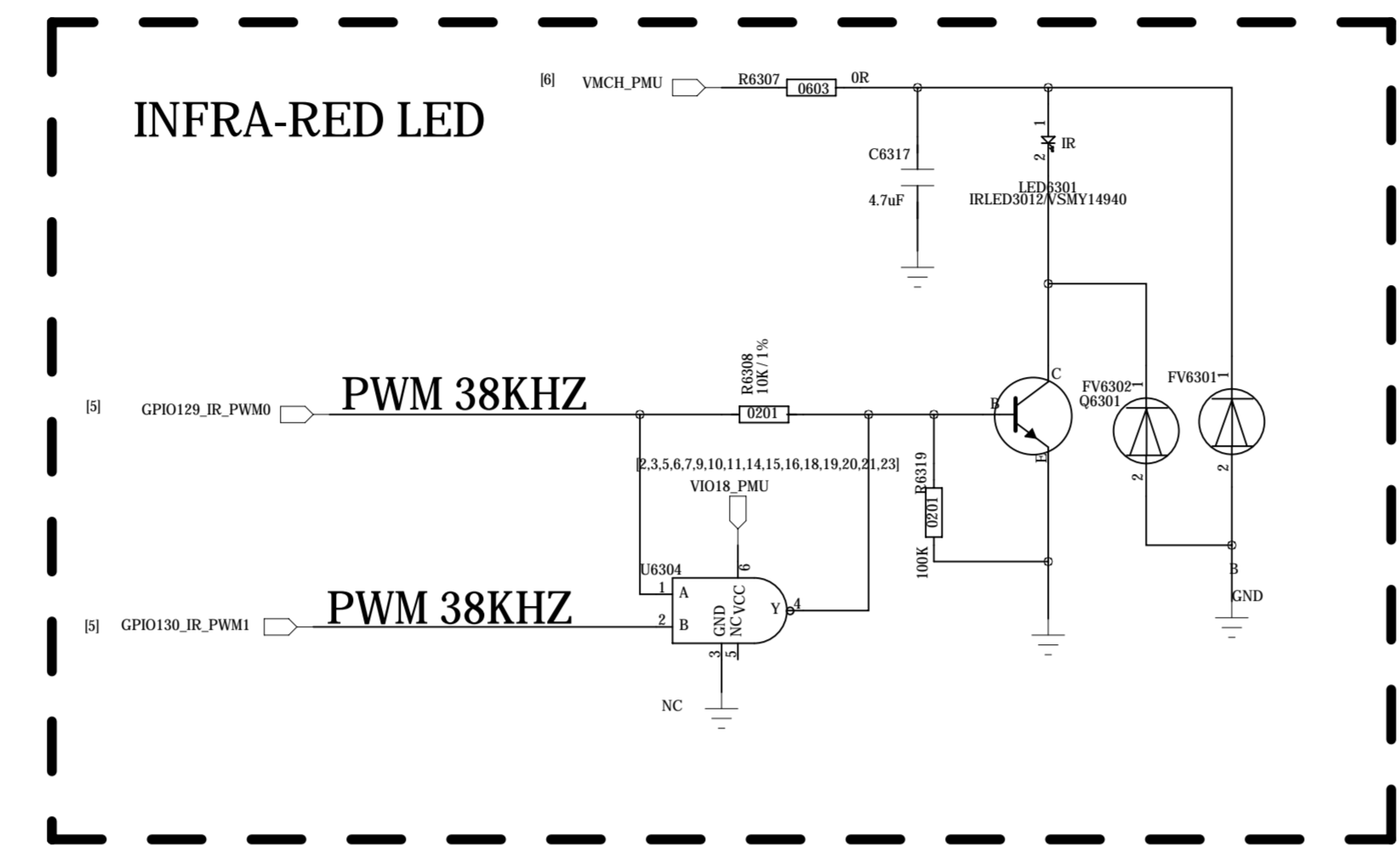
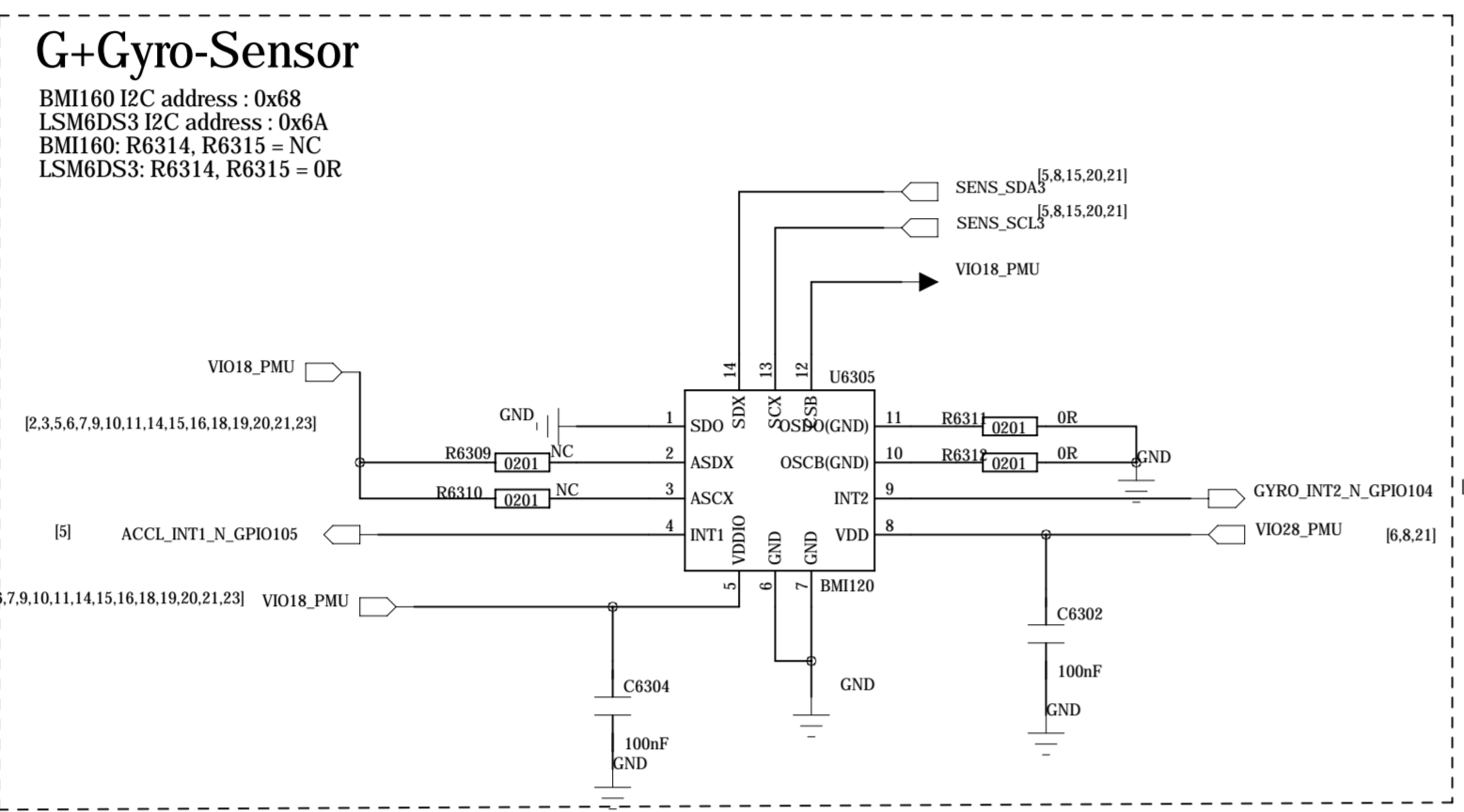
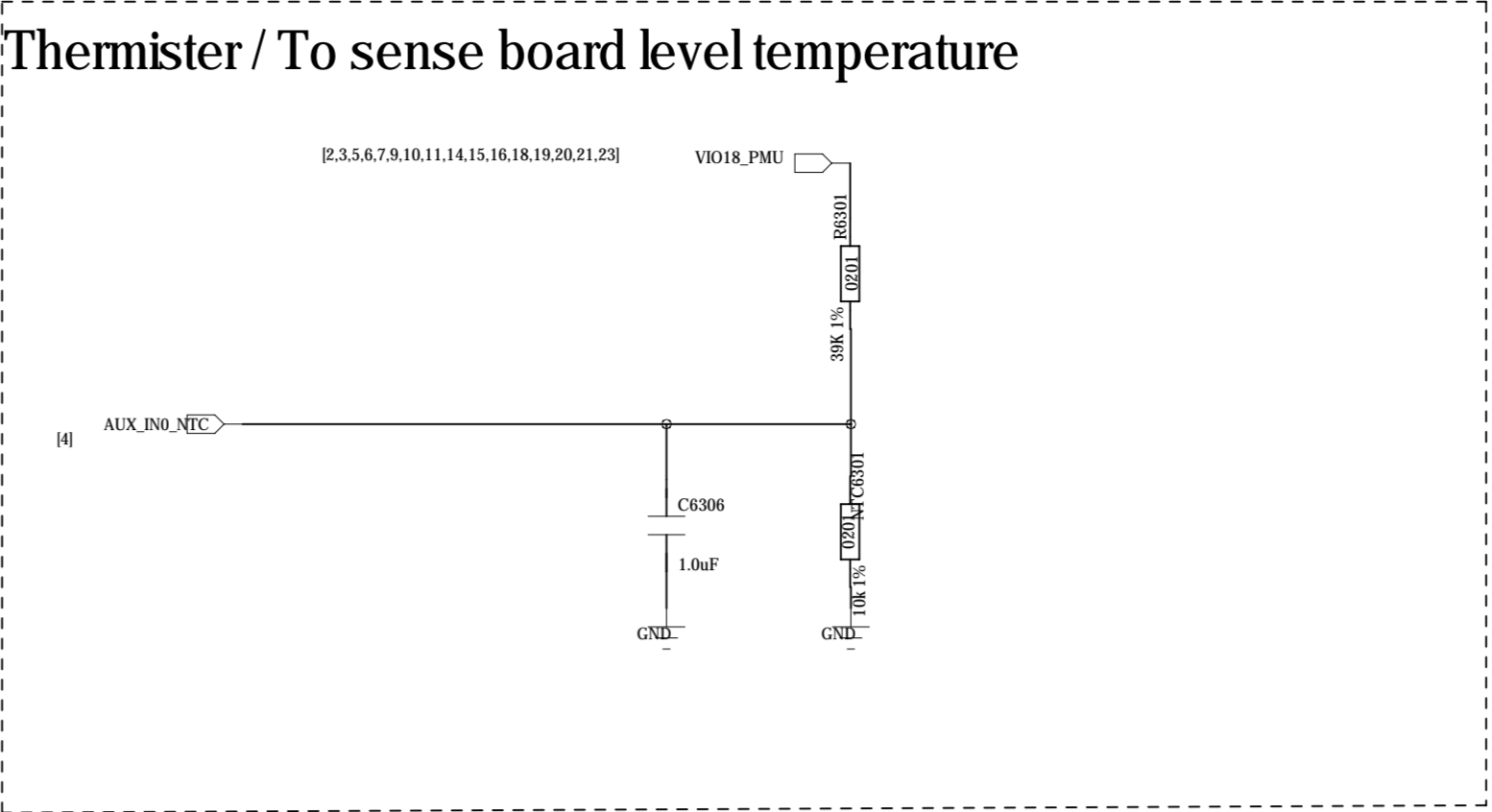
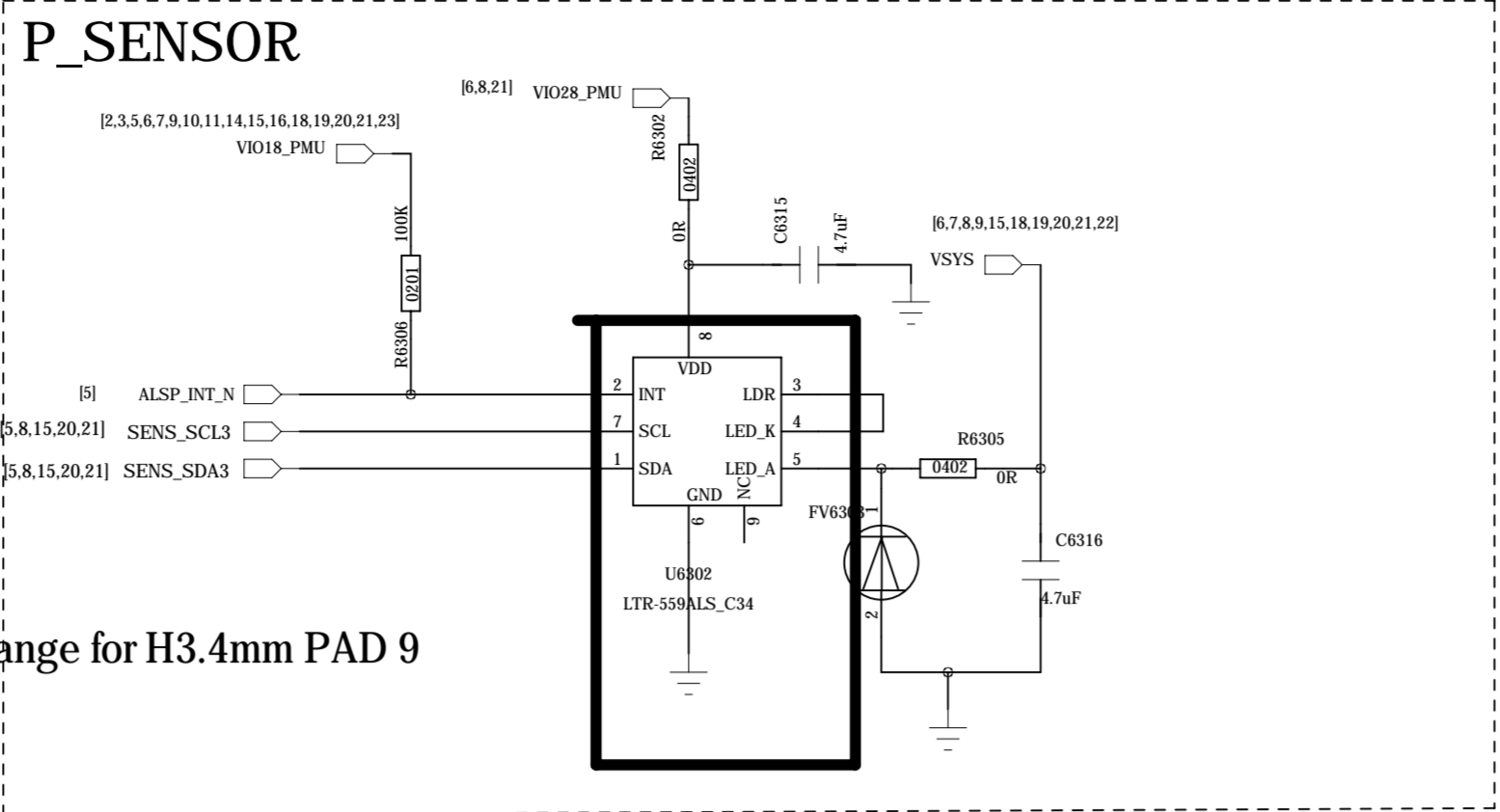
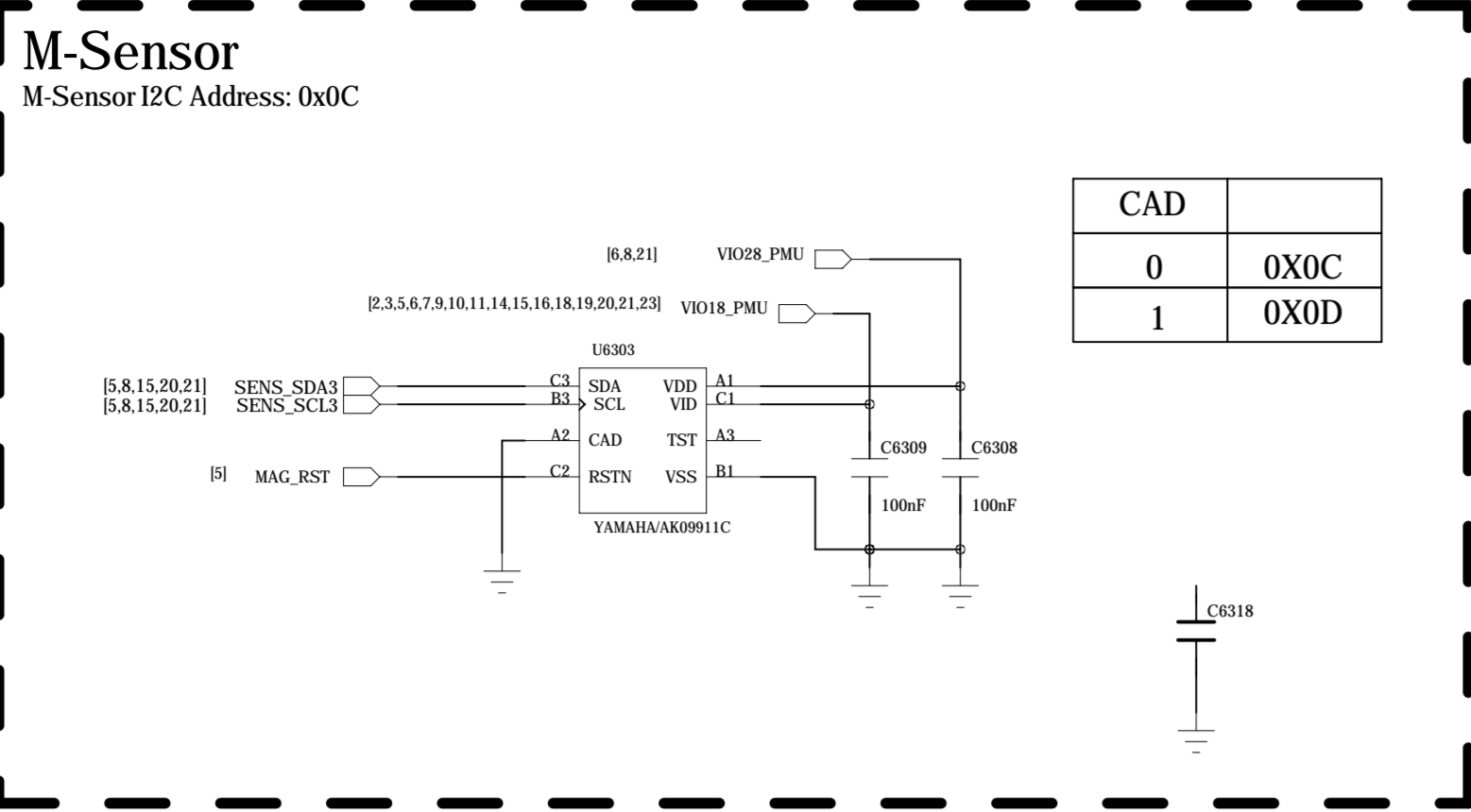
SPEAKER



C830/when use AW8145 please use 1uF CAP;when use AW8155 or AW8155A please use 0 ohm res

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